SINLOON[®] 超合金低阻值貼片電阻 Power:3W

Feature

- ♦ Stable high frequency characteristic with reduced lead inductance
- and excellent frequency response.
- Excellent frequency response.
- \blacklozenge Low thermal EMF (<1µV/°C).
- High-temperature performance (up to +170 $^{\circ}$ C).
- ♦ Very low inductance 0.5nH to 5nH
- ◆ Ideal for all types of current sensing, voltage division and pulse
- applications including switching and linear power supplies, instruments, power amplifiers.
- ◆ Pure tin plating provides compatibility with lead (Pb) and lead containing soldering processes.
- ◆ Excellent stability (I△R/RI≦±0.5% for 1000h at 100°C) different environmental conditions.

\Box Application

- ♦ Power supply ♦ Battery pack ♦ DIY tools
- ◆ Inverter / Converter (AC/DC, DC/DC, DC/AC.)
- ◆ Measurable instrument ◆ Consumer electronics
- \bullet Note book \bullet PC power pack \bullet LED driver
- ♦ Others (Auto tronics ... etc)

ORDERING INFORMATION

Example: N-LR12FER0005G (3W 2512 ±1% 100ppm m5 coating Green)

Power	Case	Туре	Tolerance	TCR(℃)	Resistance	Coating	Packing
2\\/	2512	NI P12	F = ±1%	E-+100ppm	m5=R0005	G-Groop	2K Pool
500	2012		$J = \pm 5\%$		m75=R00075	0=0leeli	211 11661

Power	Case	Туре	L	W	Т	Н	Resistance
2\\// 2512	2512 NLD12	6 25 10 254	20.02	2 69 .0 25	0.60.20	m5.	
300	2012	IN-LIXIZ	0.33±0.234	3.0±0.2	2.00 ±0.25	0.6 ±0.20	m75



	NG				MITS R
Power	Case	Туре	Resistance	Marking	10175
3\//	2512	NJI R12	m50	M50	
500	2012		m75	M75	m75

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LR Series Ultra Low Ohm Chip Resistor

Figure

Protective Coating

Metal Strip

M7.5

Solder Plating



超合金低阻值貼片電阻 Power:3W

LR Series Ultra Low Ohm Chip Resistor

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☐ GENERAL ELECTRICAL SPECIFICATION

Power	Case	Туре	Resistance Ra	nge Tolerance	TCR	Working	Overload	Point of	Plant front
at 100℃	Case	туре	F: ±1%	J: ±5%	(ppm/°C)	Current Max.	Current Max.	Solder Plating	side coating
3W	2512	N-LR12	m50 Ω	m50 Ω	±100	77.5A	173.2A	Metal Strip	Green
3W	2512	N-LR12	m75 Ω	m75 Ω	±100	63.3A	141.4A	Metal Strip	Green
Operating Temperature Range.						-55~+170 ℃			

The direct or alternating voltage for the rated power can be calculated from the following formula but must not exceed the maximum voltage. Operating Current I= $\sqrt{(P/R)}$; Operating Voltage: V= $\sqrt{(P^*R)}$

E=Rated voltage(V) E=√R×P P=Power rating(W) R=Nominal resistance(Ω)

POWER DERATING CURVE

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In case resistors operating ambient temperature in excess of the temperature range -55°C ~+170°C power ratio will be derated in accordance with the figure as shown on the right.

Power ratings are based on continuous full load operation at rated ambient temperature of 80°C,

For resistor operated at ambient temperature in excess of 80°C, the maximum load shall be derated in accordance with the following curve.

Recommend Land Pattern Design (For Reflow Soldering) Unit mm

Power	Case	Туре	Resiatance	А	B	С
ЗW	2512	N-LR12	m50	1.80	3.60	4.75
3W	2512	N-LR12	m75	1.80	3.60	4.75



SOLDERING PROFILE





超合金低阻值貼片電阻 **SINLOON[®]**

Power: 3W

LR Series

SINLOON®

Ultra Low Ohm Chip Resistor

Test Item	Procedure	Test Method	Requirements	
T	TOD (190) R2-R1 (6			
Temperature Coefifcient	$TCR (ppm/C) = \frac{1}{R1 (T2-T1)} \times 10^{\circ}$			
	R_1 : Resistance of room temperature (T_1)	JIS 5201 clause 4.8	Refer to Ratings	
of Resistance (TCR)	R_2 : Resistance at -55°C of + 125°C (T_2)			
	5 times the rated power is applied to the resistor		△R<±1% LR12 (2512)	
Short Time Overload	for 5 seconds and the change in resistance is	JIS 5201 clause 4.13	∆R<±(0.5%+0.5mΩ)	
	measured after 30 mins.			
Resistance to	The resistor is immersed solder bath at $260\pm5^{\circ}$ C			
Solder Heat	for 10±1secs and the resistance is measured	JIS 5201 clause 4.18	∆R<±(0.5%+0.5mΩ)	
	1hr after the test.			
.	The resistor is immersed in solder bath at $260\pm5^{\circ}$ C			
Solderability Test	for 2±0.5secs.	JIS 5201 clause 4.17	95% coverage	
	The resistor is kept at a temperature of -55° $\mathbb C$ for			
	15mins and the temperature is then raised to 150° C			
- i i oi i	and the resistor is held in this state for another			
Thermal Shock	15mins, This is repeated for 1000 cycles. The	JIS 5201 clause 4.21	_R<±(0.5%+0.5mΩ)	
	change in resistance is then measure 2hrs after the			
	completion of 1000 cycles.			
	The resistor is kept at a temperature of -55 $^\circ$ C for			
	5mins and the temperature us then raised to 125 $^\circ\!\!\mathbb{C}$			
	and the resistor is held in this state for another		LR12 (2512):	
Thermal Shock	5mins. The time taken for switching between	JIS 5201 clause 4.21		
	temperatures does not exceed 10secs and this is			
	repeated for 5 cycles, The change in resistance is			
	measured 2hrs after the completion of 5 cycles.			
	The resistor is placed in a chamber at -65 \pm 2 $^{\circ}$ C and the			
Low Temperature	rated voltage is applied to the resistor for 24hrs. The	110 5004	△R<±1% LR12 (2512)	
Exposure (Storage)	change in resistance is measured 60mins after removal	JIS 5201 clause 4.23-4	Not this experiment	
	from test chamber.			
	The resistor is placed in a constant temperature-			
High Temperature	humidity chamber at $170\pm2^{\circ}$ for 1000hrs and the		△R<±1% LR12 (2512)	
Exposure (Strage)	resistance is measured 60mins after the end of the	JIS 5201 clause 4.23-2	∆R<±(1.0%+0.5mΩ)	
	cycles.			
	The resistor placed in a chamber at 80-100% RH and			
	the temperature is raised from $25^\circ\mathbb{C}$ to in 2.5hrs where			
	it is kept for 3hrs after which the temperature is brought			
Moisture Resistance	down to 25° C in 2.5hrs. This 23hrs loop is repeated	110 5004 -1	△R<±1% LR12 (2512)	
(Climatic Sequence)	again and at the end of the second loop the resistor is	JIS 5201 clause 4.23	Not this experiment	
	held at 25° for the remaining 8hrs. The change in			
	resistance is then measured 2hrs after the completion	\mathbf{V} \mathbf{N} (\mathbf{R})		
	of 10 cycles.			
	The resistor is placed in a chamber for 1000hrs at			
	70 \pm 2 $^{\circ}$ C, The rated voltage is applied to the resistor		△R<±1% LR12 (2512)	
Load Life	(duty cycles: 90mins ON, 30mins OFF), The change	JIS 5201 clause 4.25-1	Not this experiment	
	in resistance is measure 60mins after removal from			
	test chamber.		×5	
	•	-	ROHS	



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LR Series Ultra Low Ohm Chip Resistor

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PACKAGE PLASTIC REEL

Туре	Plastic Reel Dimension Unit m						
	A	ΦВ	ФС	ΦD	W		
N-LR12	2±0.5	13.5±0.5	21±0.5	60±0.2	15±1		

Paper Reel Tape Packing:

Power	Case	Туре	Reel	In Box	Carton
3W	2512	N-LR12	2000 pcs	20K pcs	120K pcs
Note:	Components a	are packed in a	accordance wit	h EIA standard	d 481-1 and 48

Components are packed in accordance with EIA standard 481-1 and 481-2 Shipping quantity given is for minimum packing quantity only, For minimum order quantity, please consult the sales depatment.



Carton Pack



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% Mayloon characteristic parameters of electronic product specification changes or updates without prior notice -

